1. CPU fetches the instruction from memory according to the value of \_\_\_\_\_\_\_\_\_\_\_\_  
a) program counter  
b) status register  
c) instruction register  
d) program status word

Answer: a  
Explanation: None.

2. A memory buffer used to accommodate a speed differential is called \_\_\_\_\_\_\_\_\_\_\_\_  
a) stack pointer  
b) cache  
c) accumulator  
d) disk buffer

Answer: b  
Explanation: None.

3. Which one of the following is the address generated by CPU?  
a) physical address  
b) absolute address  
c) logical address  
d) none of the mentioned

Answer: c  
Explanation: None.

4. Run time mapping from virtual to physical address is done by \_\_\_\_\_\_\_\_\_\_\_\_  
a) Memory management unit  
b) CPU  
c) PCI  
d) None of the mentioned

Answer: a  
Explanation: None.

5. Memory management technique in which system stores and retrieves data from secondary storage for use in main memory is called?  
a) fragmentation  
b) paging  
c) mapping  
d) none of the mentioned

Answer: b  
Explanation: None.

6. The address of a page table in memory is pointed by \_\_\_\_\_\_\_\_\_\_\_\_  
a) stack pointer  
b) page table base register  
c) page register  
d) program counter

Answer: b  
Explanation: None.

7. Program always deals with \_\_\_\_\_\_\_\_\_\_\_\_  
a) logical address  
b) absolute address  
c) physical address  
d) relative address

Answer: a  
Explanation: None.

8. The page table contains \_\_\_\_\_\_\_\_\_\_\_\_  
a) base address of each page in physical memory  
b) page offset  
c) page size  
d) none of the mentioned

Answer: a  
Explanation: None.

9. Operating System maintains the page table for \_\_\_\_\_\_\_\_\_\_\_\_  
a) each process  
b) each thread  
c) each instruction  
d) each address

Answer: a  
Explanation: None.